



Day : Friday
Date: 10/3/2003
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Inventor Name Search Result

Your Search was:

Last Name = IRETON

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09993982</u>	Not Issued	030	11/16/2001	USE OF DATABASE QUERIES FOR MANIPULATION OF MEDIA CONTENT	IRETON, MARK
<u>09992091</u>	Not Issued	030	11/16/2001	REMOTE-DIRECTED MANAGEMENT OF MEDIA CONTENT	IRETON, MARK
<u>09783244</u>	Not Issued	030	02/13/2001	ENSURING LEGITIMACY OF DIGITAL MEDIA	IRETON, MARK
<u>09747351</u>	Not Issued	071	12/19/2000	ENABLING PROTECTED DIGITAL MEDIA TO BE SHARED BETWEEN PLAYBACK DEVICES	IRETON, MARK
<u>09008967</u>	<u>5970447</u>	150	01/20/1998	SYSTEM AND METHOD FOR DETECTING VOICE ACTIVITY BASED ON INTEGRATED ABSOLUTE VALUES OF THE DIFFERENTIATED ZERO CROSSING RATE	IRETON , MARK A.
<u>08957595</u>	<u>6026357</u>	150	10/24/1997	FIRST FORMANT LOCATION DETERMINATION AND REMOVAL FROM SPEECH CORRELATION INFORMATION FOR PITCH DETECTION	IRETON , MARK A.
<u>08957099</u>	<u>6047254</u>	150	10/24/1997	SYSTEM AND METHOD FOR DETERMINING A FIRST FORMANT ANALYSIS FILTER AND PREFILTERING A SPEECH SIGNAL FOR IMPROVED PITCH ESTIMATION	IRETON , MARK A
<u>08851411</u>	<u>5745648</u>	150	05/05/1997	APPARATUS AND METHOD FOR ANALYZING SPEECH SIGNALS TO DETERMINE PARAMETERS EXPRESSIVE OF CHARACTERISTICS OF THE SPEECH SIGNALS	IRETON , MARK A.
<u>08759611</u>	<u>5901225</u>	150	12/05/1996	SYSTEM AND METHOD FOR PERFORMING SOFTWARE PATCHES IN EMBEDDED SYSTEMS	IRETON , MARK A.
<u>08707700</u>	<u>5797120</u>	150	09/04/1996	SYSTEM AND METHOD FOR GENERATING RE-CONFIGURABLE BAND LIMITED NOISE USING MODULATION	IRETON , MARK A.

<u>08672475</u>	Not Issued	092	06/28/1996	MICROPROCESSOR CONFIGURED TO TRANSLATE INSTRUCTIONS FROM ONE INSTRUCTION SET TO ANOTHER, AND TO STORE THE TRANSLATED INSTRUCTIONS	IRETON , MARK A.
<u>08651261</u>	<u>5896393</u>	150	05/23/1996	SIMPLIFIED FILE MANAGEMENT SCHEME FOR FLASH MEMORY	IRETON , MARK
<u>08647843</u>	<u>5937374</u>	150	05/15/1996	SYSTEM AND METHOD FOR IMPROVED PITCH ESTIMATION WHICH PERFORMS FIRST FORMANT ENERGY REMOVAL FOR A FRAME USING COEFFICIENTS FROM A PRIOR FRAME	IRETON , MARK A.
<u>08643522</u>	<u>5778337</u>	150	05/06/1996	DISPERSED IMPULSE GENERATOR SYSTEM AND METHOD FOR EFFICIENTLY COMPUTING AN EXCITATION SIGNAL IN A SPEECH PRODUCTION MODEL	IRETON , MARK A.
<u>08643344</u>	<u>5721945</u>	150	05/06/1996	MICROPROCESSOR CONFIGURED TO DETECT A DSP CALL INSTRUCTION AND TO DIRECT A DSP TO EXECUTE A ROUTINE CORRESPONDING TO THE DSP CALL INSTRUCTION	IRETON , MARK A.
<u>08633352</u>	<u>6092094</u>	150	04/17/1996	EXECUTE UNIT CONFIGURED TO SELECTABLY INTERPRET AN OPERAND AS MULTIPLE OPERANDS OR AS A SINGLE OPERAND	IRETON , MARK A.
<u>08626728</u>	<u>5774836</u>	150	04/01/1996	SYSTEM AND METHOD FOR PERFORMING PITCH ESTIMATION AND ERROR CHECKING ON LOW ESTIMATED PITCH VALUES IN A CORRELATION BASED PITCH ESTIMATOR	IRETON , MARK
<u>08618243</u>	<u>5794068</u>	150	03/18/1996	CPU WITH DSP HAVING FUNCTION PREPROCESSOR THAT CONVERTS INSTRUCTION SEQUENCES INTENDED TO PERFORM DSP FUNCTION INTO DSP FUNCTION IDENTIFIER	IRETON , MARK
<u>08618242</u>	<u>5754878</u>	150	03/18/1996	CPU WITH DSP FUNCTION PREPROCESSOR HAVING PATTERN RECOGNITION DETECTOR THAT USES TABLE FOR TRANSLATING INSTRUCTION SEQUENCES INTENDED TO PERFORM DSP FUNCTION INTO DSP MACROS	IRETON , MARK
<u>08618241</u>	<u>5784640</u>	150	03/18/1996	CPU WITH DSP FUNCTION PREPROCESSOR HAVING LOOK-UP TABLE FOR TRANSLATING INSTRUCTION SEQUENCES	IRETON , MARK

				INTENDED TO PERFORM DSP FUNCTION INTO DSP MACROS	
<u>08618240</u>	<u>5790824</u>	150	03/18/1996	CENTRAL PROCESSING UNIT INCLUDING A DSP FUNCTION PREPROCESSOR WHICH SCANS INSTRUCTION SEQUENCES FOR DSP FUNCTIONS	IRETON , MARK
<u>08618000</u>	<u>5781792</u>	150	03/18/1996	CPU WITH DSP HAVING DECODER THAT DETECTS AND CONVERTS INSTRUCTION SEQUENCES INTENDED TO PERFORM DSP FUNCTION INTO DSP FUNCTION IDENTIFIER	IRETON , MARK
<u>08583193</u>	<u>5649138</u>	150	01/04/1996	TIME DEPENDENT REROUTING OF INSTRUCTIONS IN PLURALITY OF RESERVATION STATIONS OF A SUPERSCALAR MICROPROCESSOR	IRETON , MARK A.
<u>08583157</u>	<u>5790880</u>	150	01/04/1996	MICROPROCESSOR CONFIGURED TO DYNAMICALLY CONNECT PROCESSING ELEMENTS ACCORDING TO DATA DEPENDENCIES	IRETON , MARK A.
<u>08583154</u>	<u>5826089</u>	150	01/04/1996	INSTRUCTION TRANSLATION UNIT CONFIGURED TO TRANSLATE FROM A FIRST INSTRUCTION SET TO A SECOND INSTRUCTION SET	IRETON , MARK A.
<u>08556262</u>	<u>5673361</u>	150	11/13/1995	SYSTEM AND METHOD FOR PERFORMING PREDICTIVE SCALING IN COMPUTING LPC SPEECH CODING COEFFICIENTS	IRETON , MARK A.
<u>08399497</u>	<u>5991725</u>	150	03/07/1995	SYSTEM AND METHOD FOR ENHANCED SPEECH QUALITY IN VOICE STORAGE AND RETRIEVAL SYSTEMS	IRETON , MARK
<u>08318379</u>	Not Issued	166	10/05/1994	APPARATUS AND METHOD FOR LOCATING A PLURALITY OF ROOTS OF A LINE SPECTRUM PAIR EXPRESSION ON UNIT CIRCLE	IRETON , MARK A.
<u>08280297</u>	<u>5459750</u>	150	07/26/1994	APPARATUS AND METHOD FOR DISCRIMINATING AND SUPPRESSING NOISE WITHIN AN INCOMING SIGNAL	IRETON , MARK A.
<u>08217309</u>	<u>5615139</u>	150	03/23/1994	APPARATUS AND METHOD FOR SYNTHESIZING A SINUSOIDAL SIGNAL	IRETON , MARK A.
<u>08051571</u>	<u>5255105</u>	150	04/22/1993	ENCODING IMAGE DATA	IRETON , MARK A.
<u>07887470</u>	Not Issued	166	05/22/1992	APPARATUS AND METHOD FOR DISCRIMINATING AND SUPPRESSING NOISE WITHIN AN INCOMING SIGNAL	IRETON , MARK A.

<u>07659020</u>	Not Issued	166	02/21/1991	ENCODING IMAGE DATA	IRETON , MARK A.
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